

2nd func	GPIO	schematic	pin	pin	schematic	GPIO	2nd func	3rd func	4th func
		+3v3	1	2	5V_IN				
	PD30	I2C1_SDA	3	4	5V_IN				
	PD31	I2C1_SCK	5	6	GND				
UART1_TXD	PD28	GPIO1	7	8	UART0_TXD	PF3			
		GND	9	10	UART0_RXD	PF0	GPS_CLK		
UART1_RXD	PD26	GPIO2	11	12	PWM	PE5	PWM5	UART3_TXD	SCLK_RSTN
UART1_CTS	PD27	GPIO3	13	14	GND				
UART1_RTS	PD29	GPIO4	15	16	GPIO5	PF1	UART0_CTS	GPS_MAG	
		+3v3	17	18	GPIO6	PF2	UART0_RTS	GPS_SIG	
SSI1_DT	PE17	SSI0_DT	19	20	GND				
SSI1_DR	PE14	SSI0_DR	21	22	GPIO7	PE8	UART3_CTS	BCLK_AD	
SSI1_CLK	PE15	SSI0_CLK	23	24	SSI0_CE0	PE16	SSI1_CE0		
		GND	25	26	SSI0_CE1	PE18	SSI1_CE1		

Key	Power	UART0	UART1	UART3	SSI	GPS	I2C	I2S
	GPIO	PWM					Version	1.5

- 1 - Note that the PWM facility is unavailable as this PWM unit is used by the Linux kernel SMP timer code
- 2 - Note that the GPS interface is unavailable as it is (believed to be) electrically in compliant
- 3 - Note that the I2S block is not useable on the header as not all I2S pins are brought out